Title: FIB Circuit Edit Design and Planning of the Circuit Modification Procedure

Training duration: 2 days

Training cost: Groups of 1 – 5 trainees, US$2400 per group

Background required: Both “Basic FIB Circuit Modification - Front Side” and “Basic FIB Circuit Modification – Backside” training or equivalent knowledge

Student Objective: Know and understand major components and layers of modern CMOS microelectronic circuits, basics of “FIB-friendly” circuit layout, limitations of FIB circuit editing process, rules of successful circuit modification.


Syllabus: Available online: www.partbeamsystech.com/training